Attorney Docket No. 1450.1040

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Tetsuo KAWANO, et al.

Application No.: 10/807,286

Confirmation No.: 3924

Filed: March 24, 2004

U.S. Patent No.: 7,219,320

Issued: May 15, 2007

ATTENTION

CERTIFICATE OF CORRECTION

BRANCH

For: SEMICONDUCTOR INTEGRATED CIRCUIT TIMING ANALYSIS APPARATUS, TIMING ANALYSIS METHOD AND TIMING ANALYSIS PROGRAM (as Amended)

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Patentees respectfully request that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistakes shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: July 25, 2007

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PTO/SB/ 44 (08-00)
Approved for use through 09/30/2000. OMB 0651-0033
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 7,219,320

DATED : May 15, 2007

INVENTOR(S) : Tetsuo KAWANO, et al.

First Page, Column 1 (Title), Line 2, change "APPARATUS" to --APPARATUS,--.

Column 1 (Title), Line 2, change "APPARATUS" to --APPARATUS,--.

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